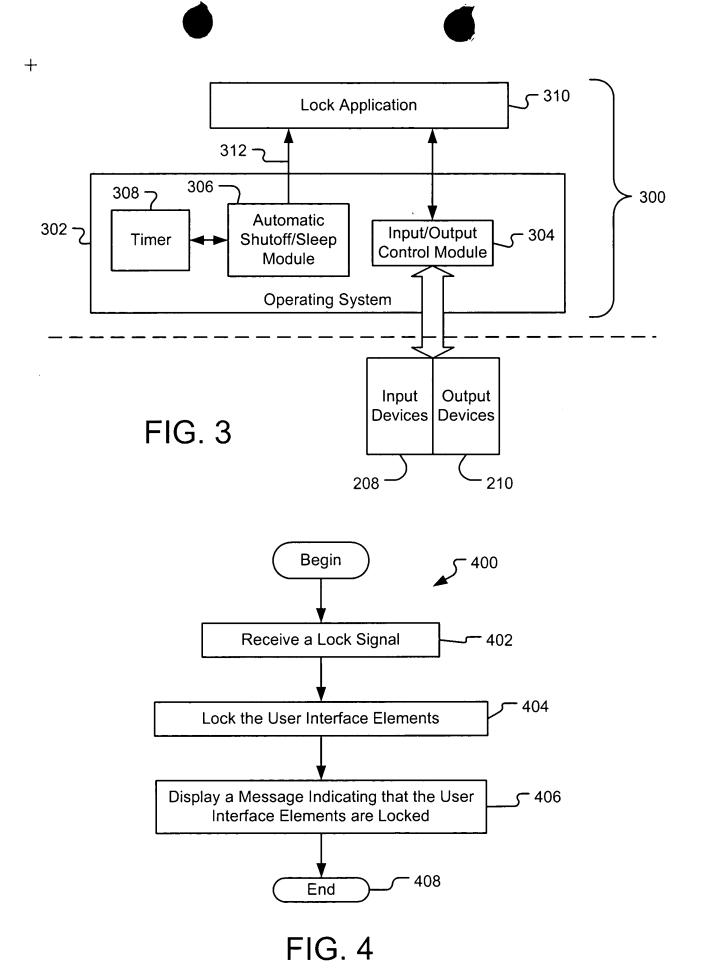


Fig. 1 - 200 202 - 212 Input ALU Main Memory Device ≥ ₂₁₄ - 220 212 Registers 222 - 208 216 Secondary - 218 **-** 212 Storage Control Unit Output Device Memory System **CPU** - 204 ∠₂₁₀ 206 -Computer

Fig. 2



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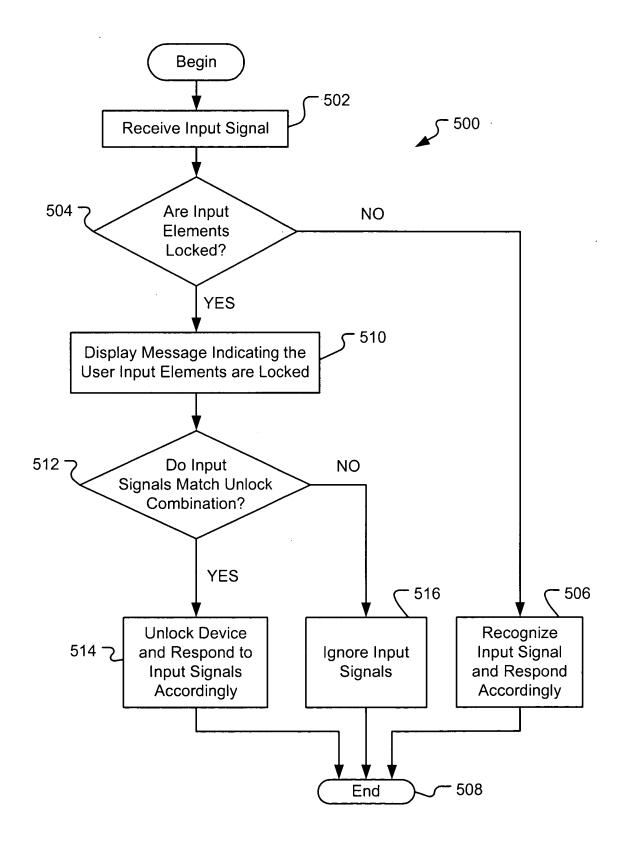


FIG. 5